


<b>Search Notes</b>  	<b>Application/Control No.</b>  10573970	<b>Applicant(s)/Patent Under Reexamination</b>  HIRAMATSU ET AL.
	<b>Examiner</b>  Giroux, George D	<b>Art Unit</b>  2183

SEARCHED			
Class	Subclass	Date	Examiner
716	16	11-18-08	GG
712	11	11-18-08	GG

SEARCH NOTES		
Search Notes	Date	Examiner
references found in examiner's case 11/277764	7-7-07	GG
PALM Inventor Name Search: Hiramatsu, Tatsuo; Nakajima, Hiroshi; Okada, Makoto and Ozone, Makoto	7-7-07	GG
new EAST search, attached	5-8-08	GG
Google Scholar Searches	5-8-08	GG
updated EAST search, attached	11-18-08	GG
Google Scholar Searches: reconfigur* manager/control, reconfigur* row, etc.	11-18-08	GG
updated EAST searches, attached	5-7-09	GG
EAST - see attached updated search document	11-28-09	GG
Google Scholar/IEEE - striped/time-multiplexed FPGA searches and context switching	11-28-09	GG
Consulted with Primary Examiner Aimee Li	11-28-09	GG

INTERFERENCE SEARCH			
Class	Subclass	Date	Examiner

/GEORGE D GIROUX/  
Examiner, Art Unit 2183